

FIG. 1

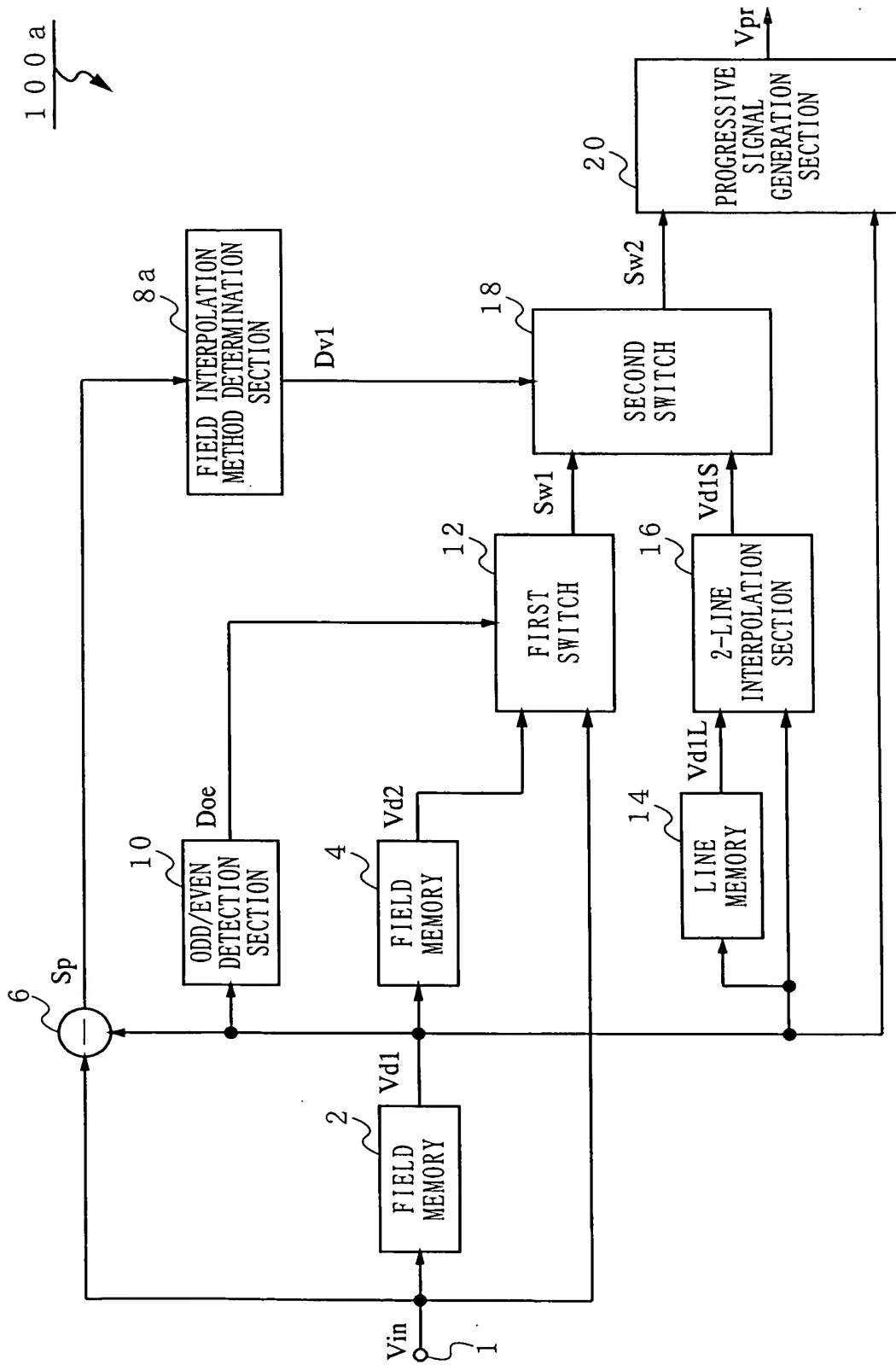


FIG. 2

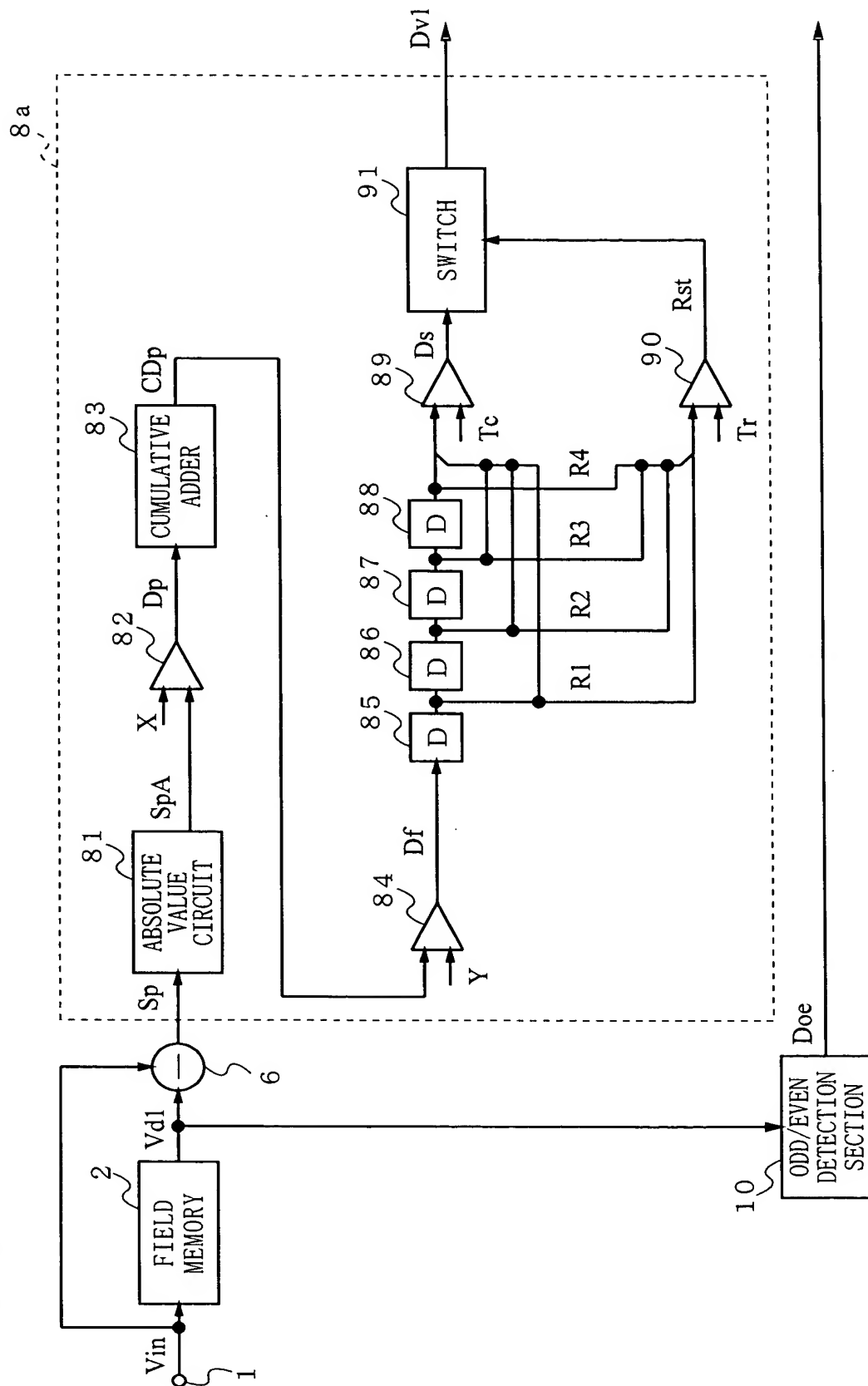


FIG. 3

REGISTER 8 5	REGISTER 8 6	REGISTER 8 7	REGISTER 8 8	COUNTER INPUT	
R1	R2	R3	R4		
0	1	0	1	INCREMENT	T _c
1	0	1	0	INCREMENT	
0	0	0	0	CANCEL	T ₁
0	0	0	1	CANCEL	
0	0	1	0	CANCEL	
0	1	0	0	CANCEL	
1	0	0	0	CANCEL	
0	0	1	1	RESET	T _r
0	1	1	0	RESET	
0	1	1	1	RESET	
1	0	0	1	RESET	
1	0	1	1	RESET	
1	1	0	0	RESET	
1	1	0	1	RESET	
1	1	1	0	RESET	
1	1	1	1	RESET	

FIG. 4

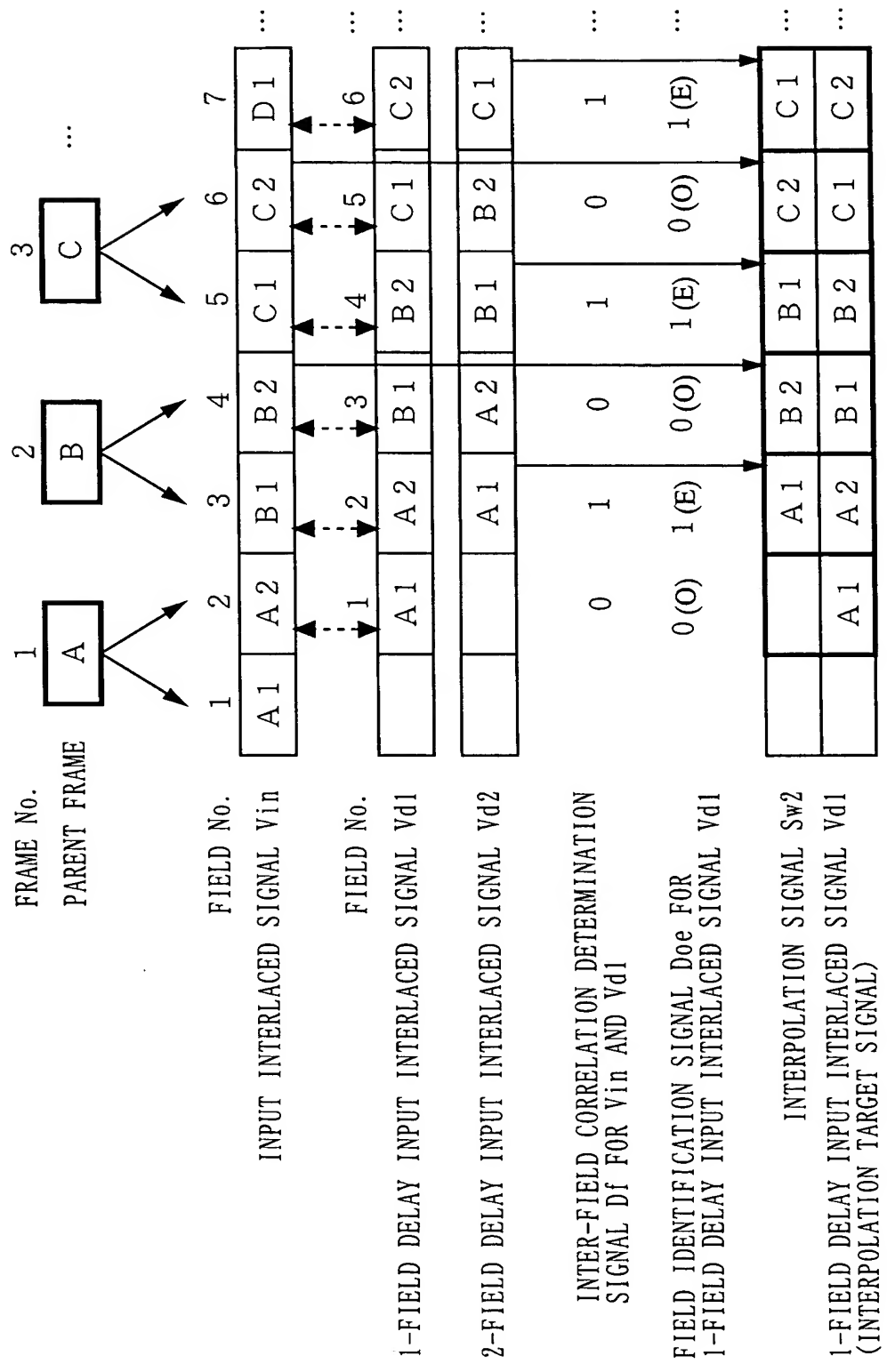


FIG. 5

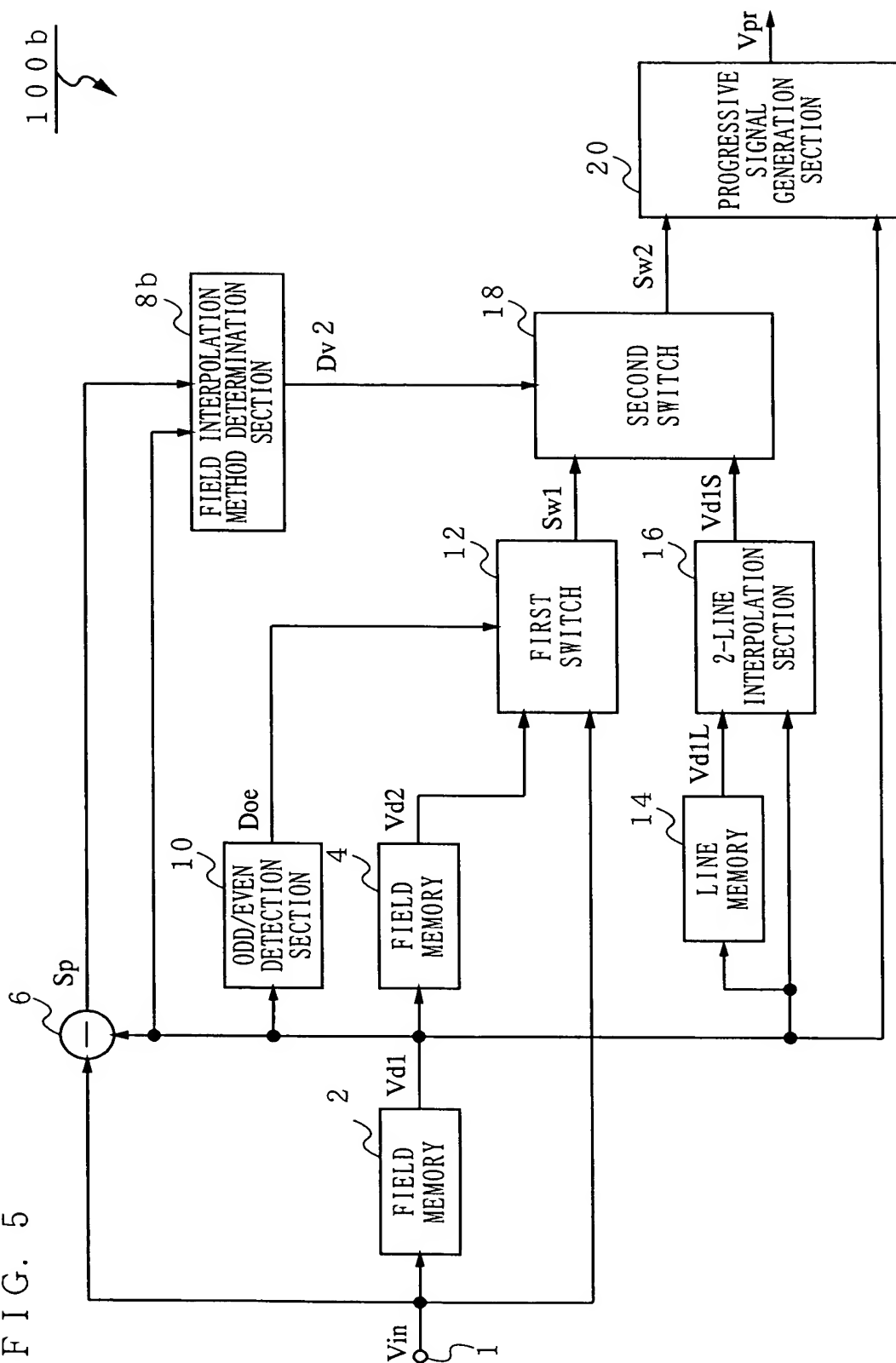


FIG. 6

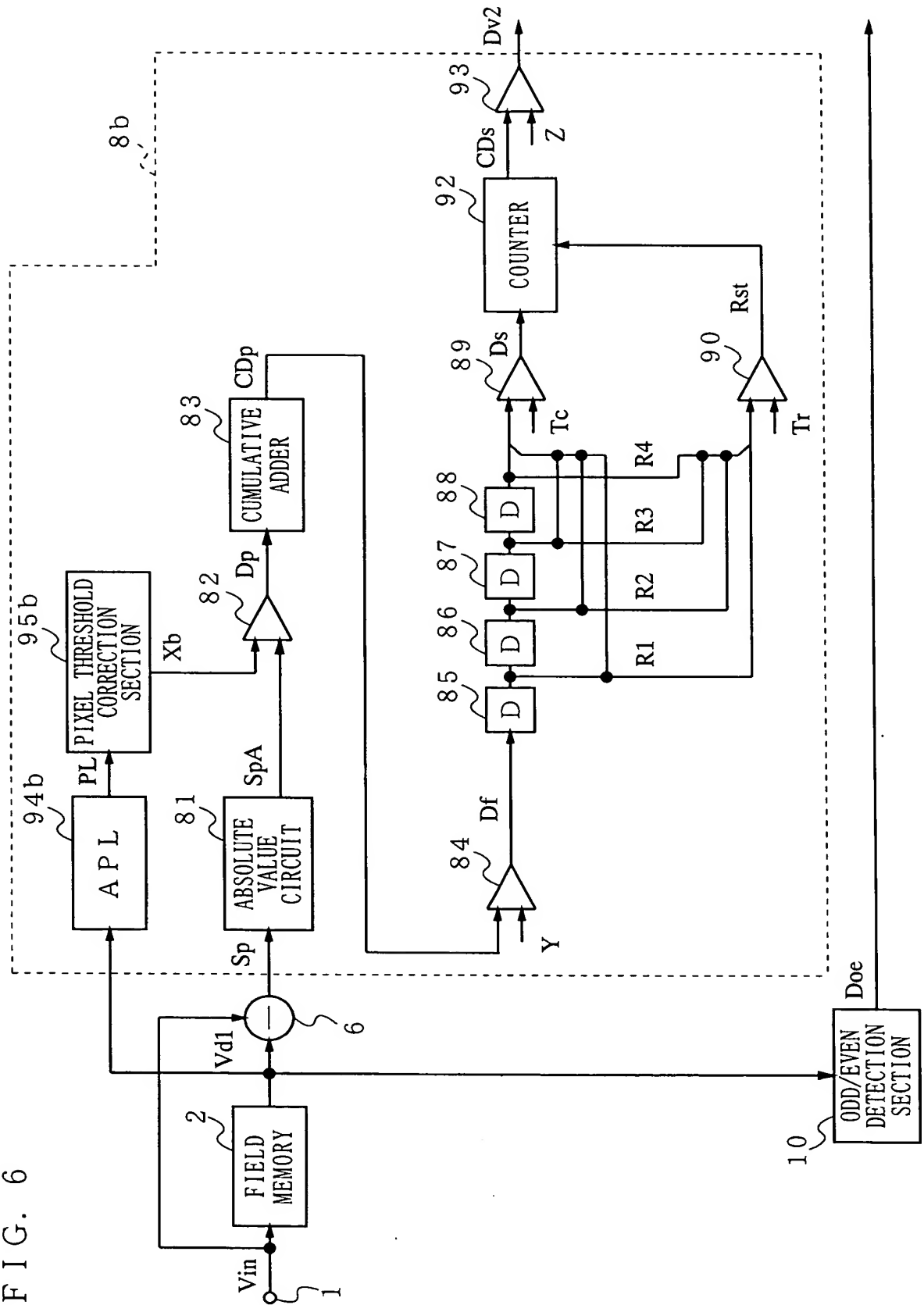


FIG. 7

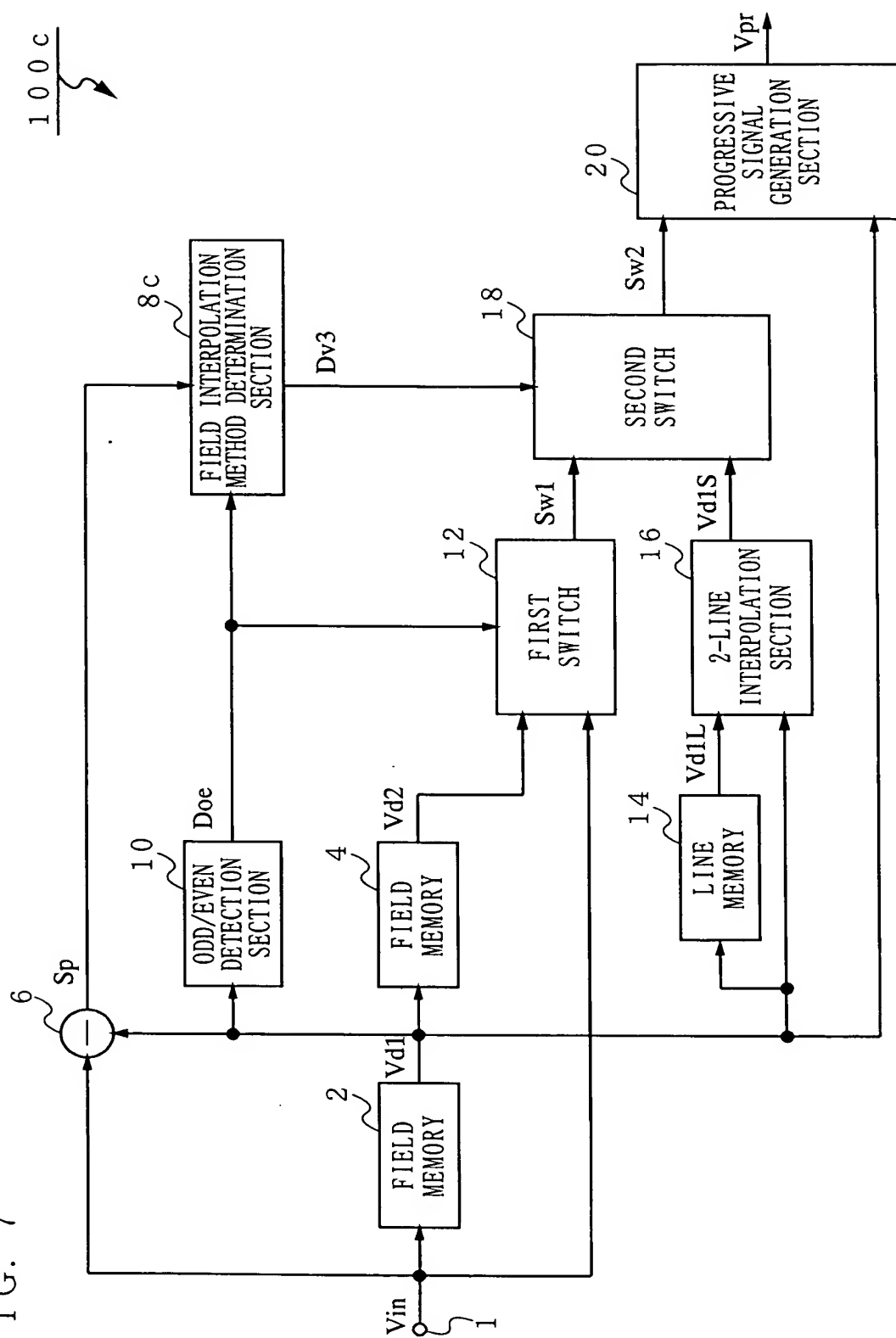


FIG. 8

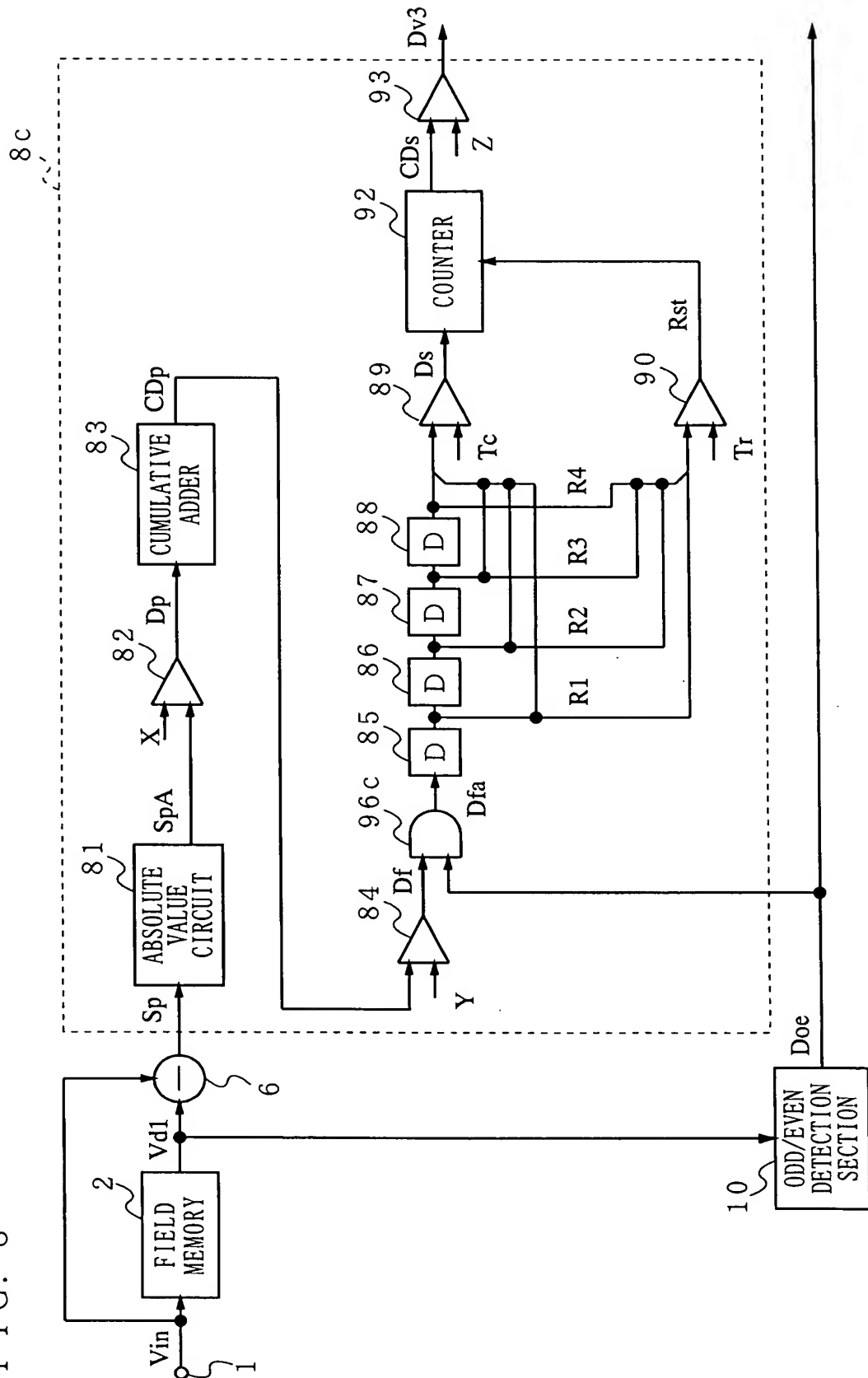


FIG. 9

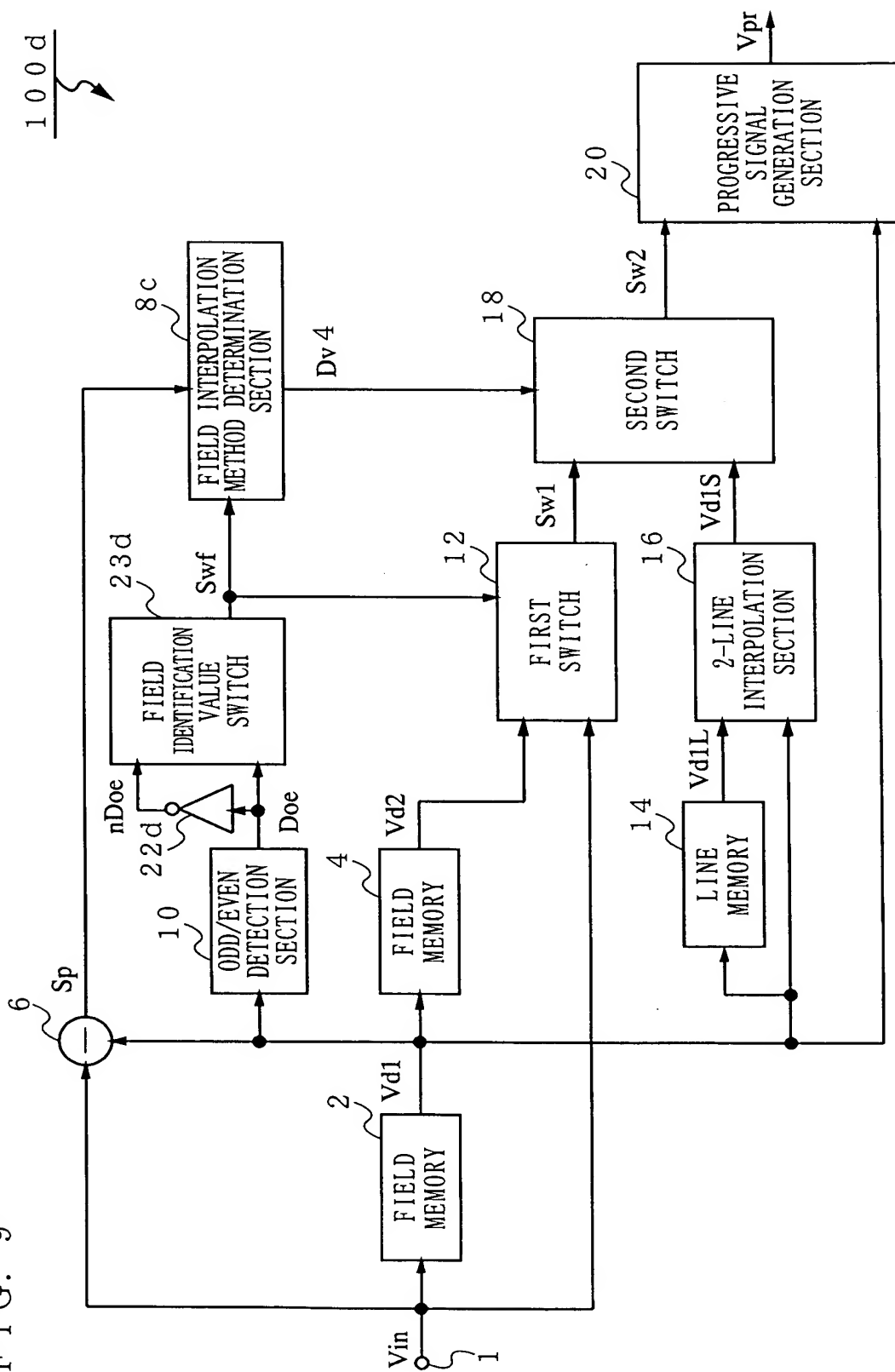


FIG. 10

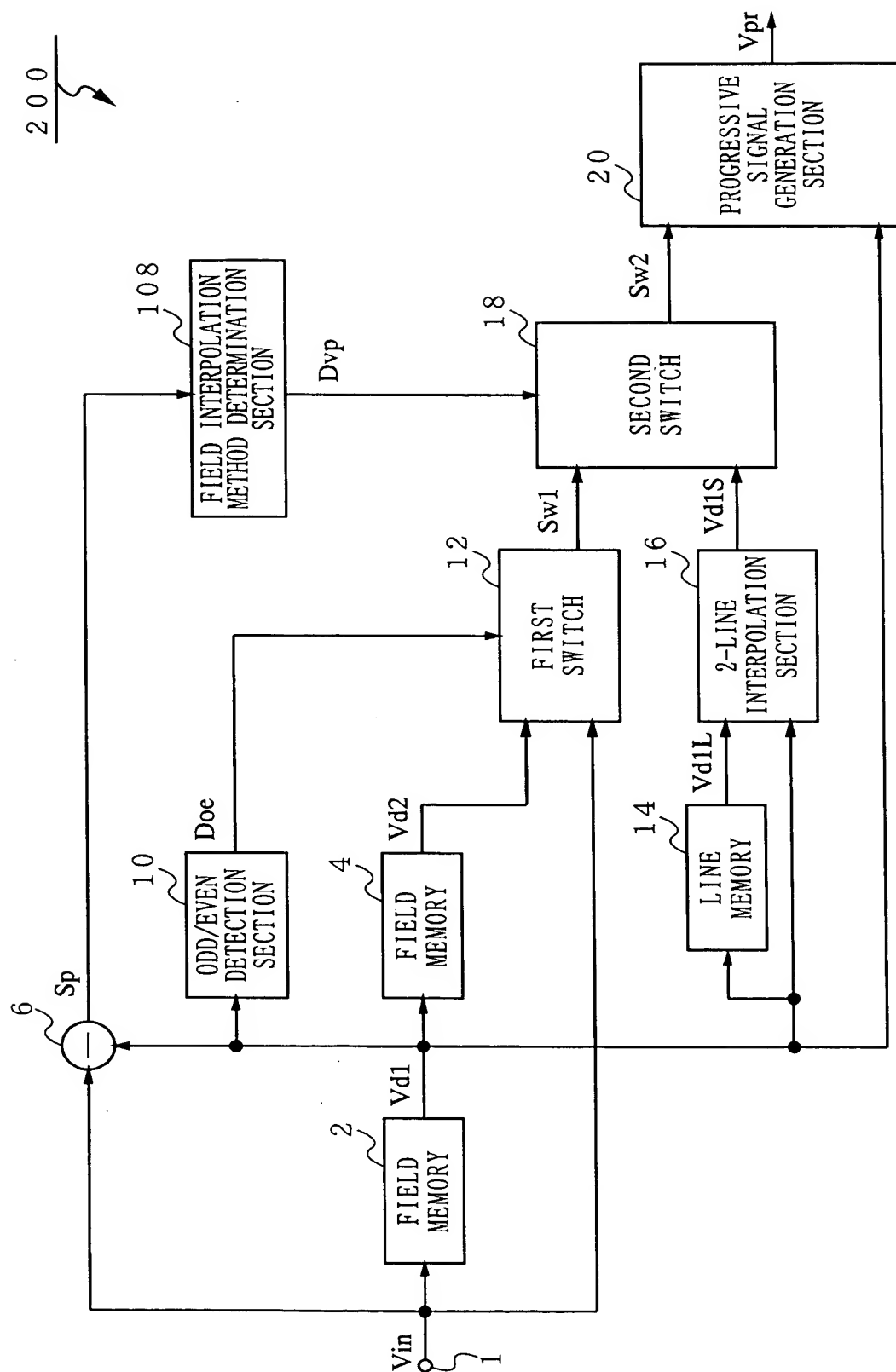


FIG. 11

